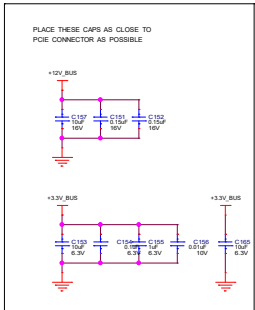



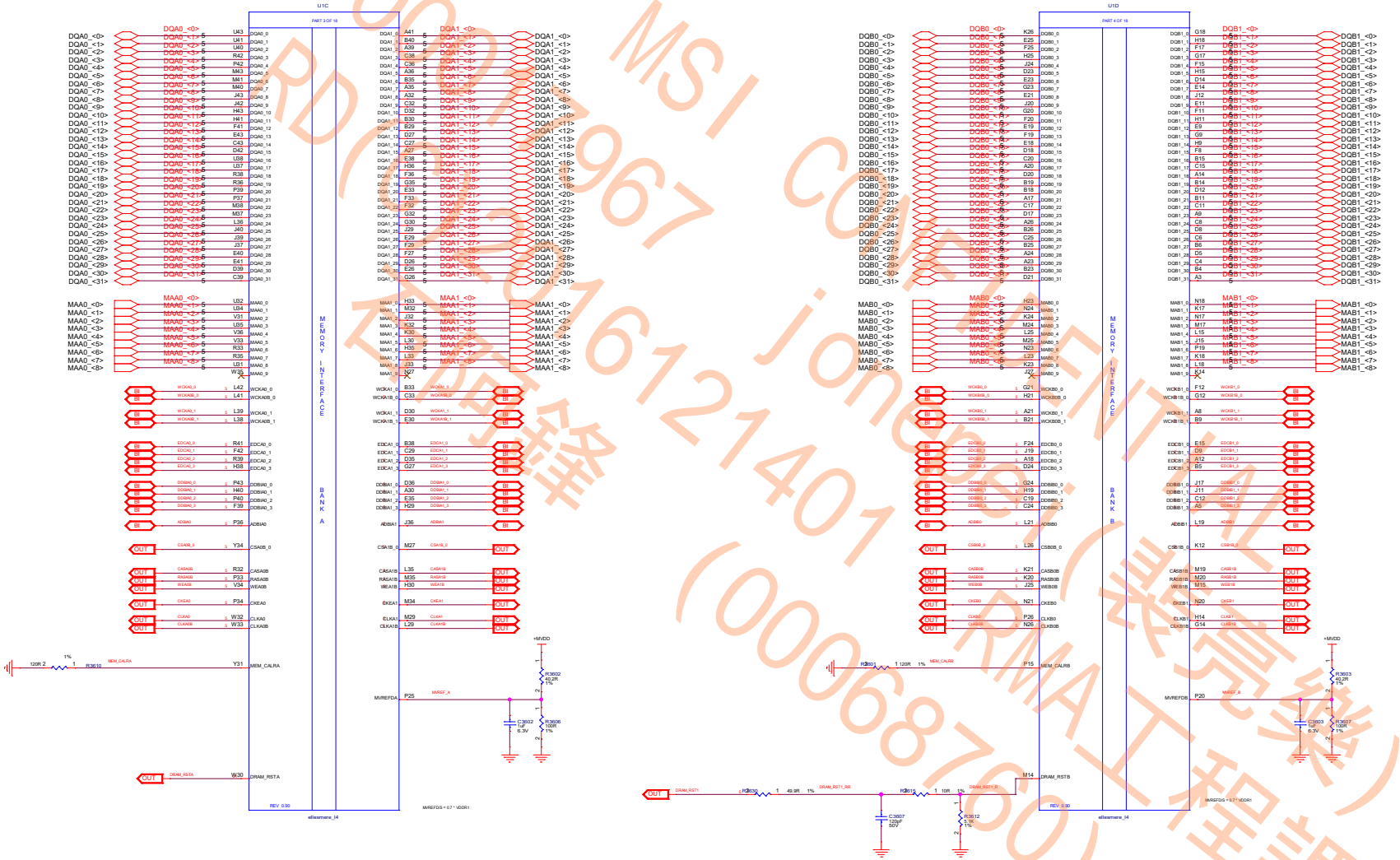
(1) PCI-EXPRESS EDGE CONNECTOR



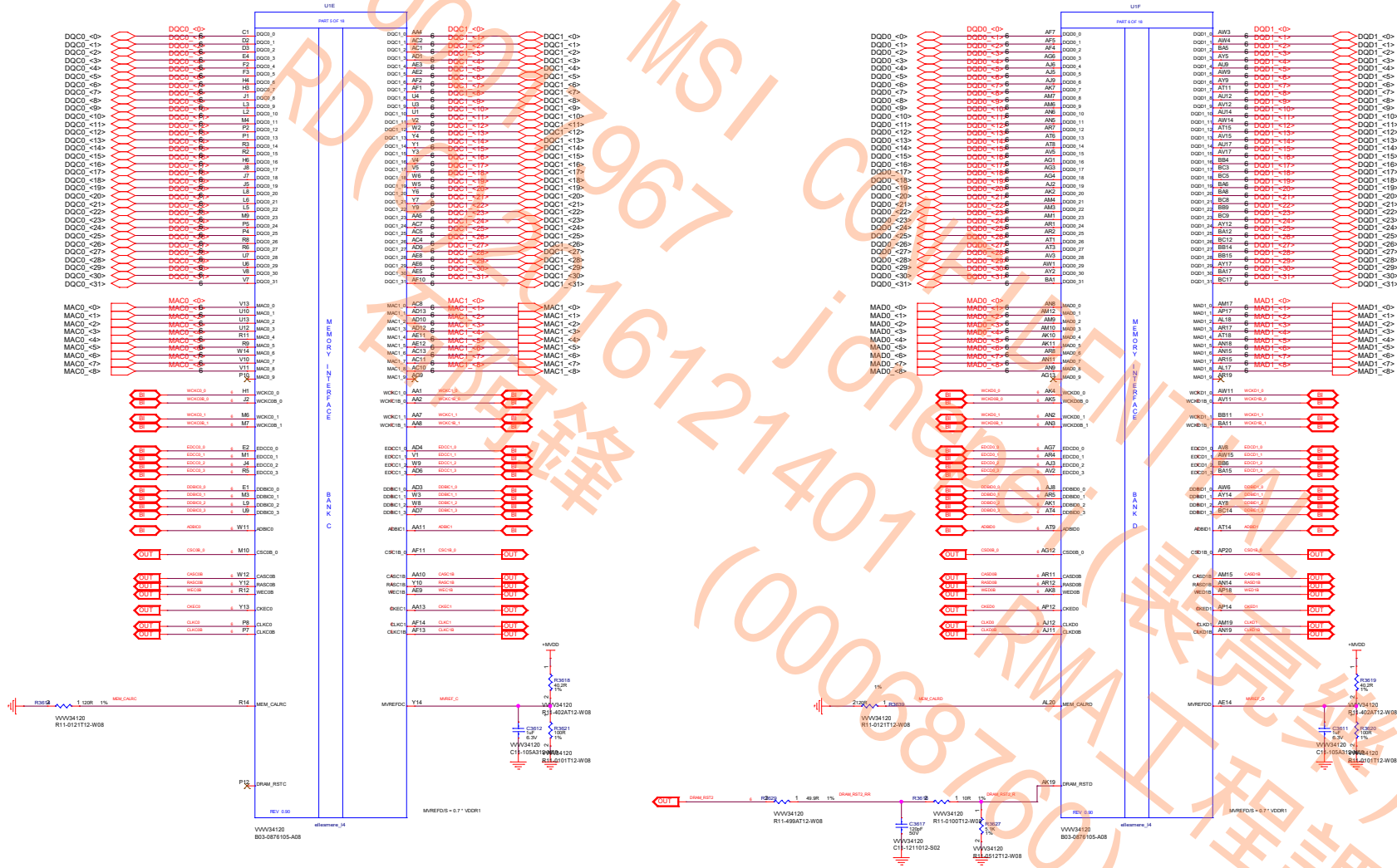
	MICRO-STAR INT'L CO.,LTD		
	MS-V341		
	Size Custom	Document Description 0001 PCIE EDGE CONNECTOR	Re 2
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[illegible]

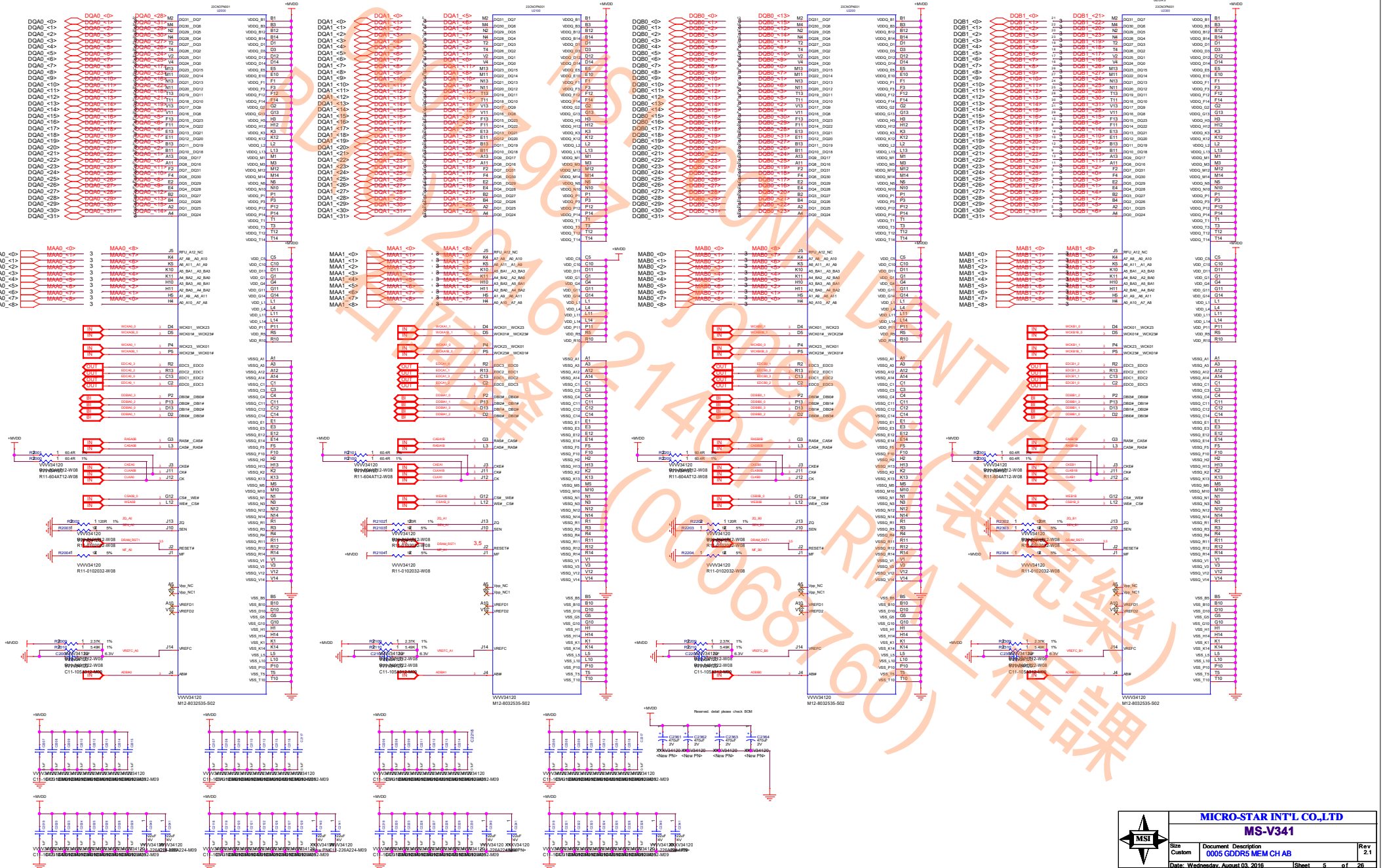
(3) ELLESMERE MEM INTERFACE CH A/B



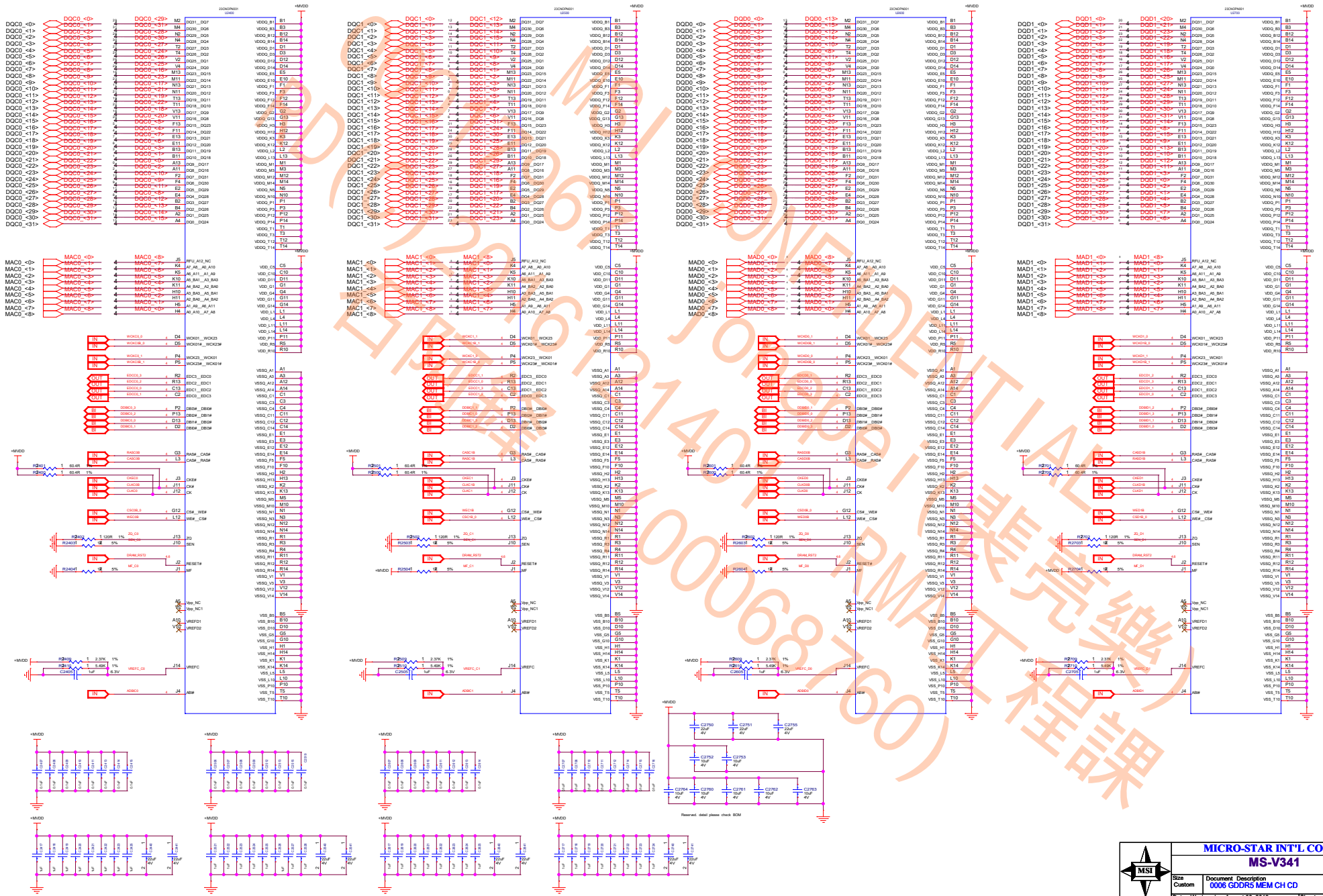
(4) ELLESMERE MEM INTERFACE CH C/D



(5) GDDR5 MEMORY CH A/B

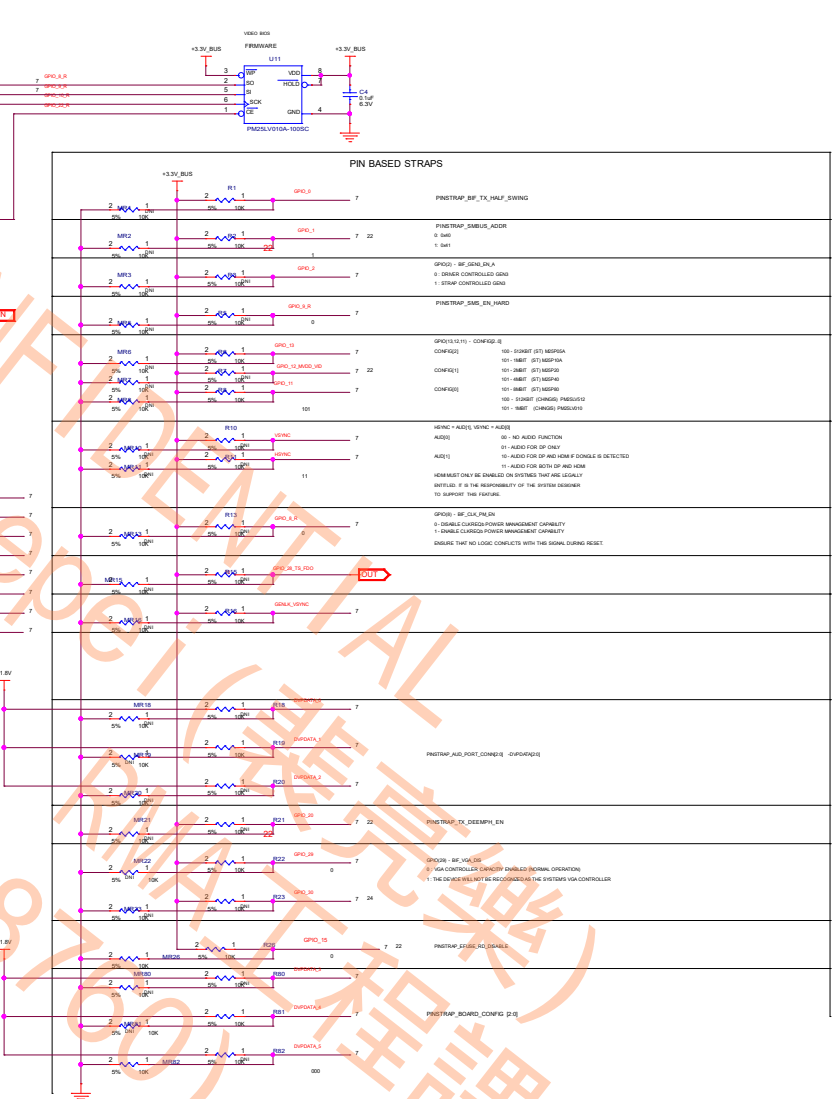
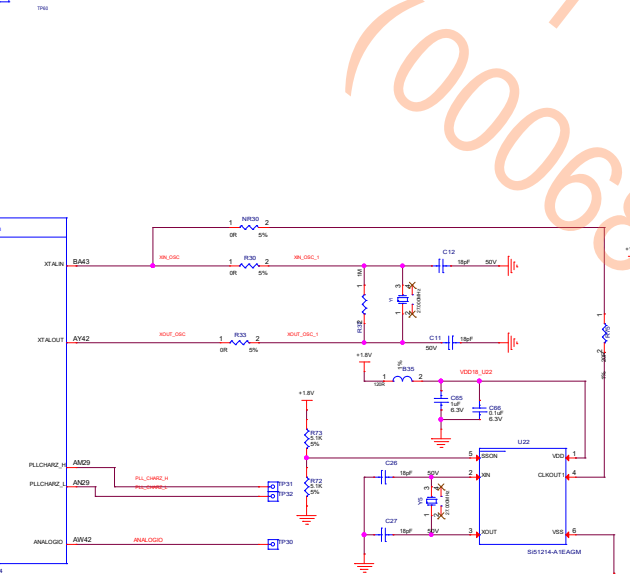
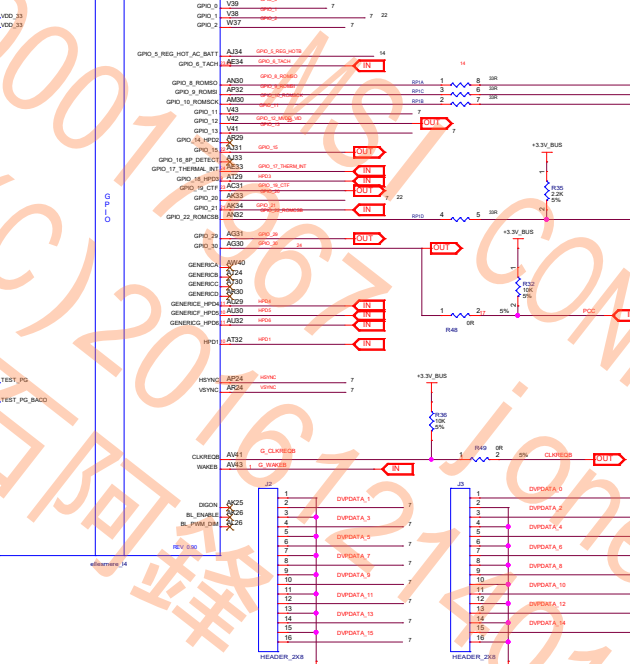
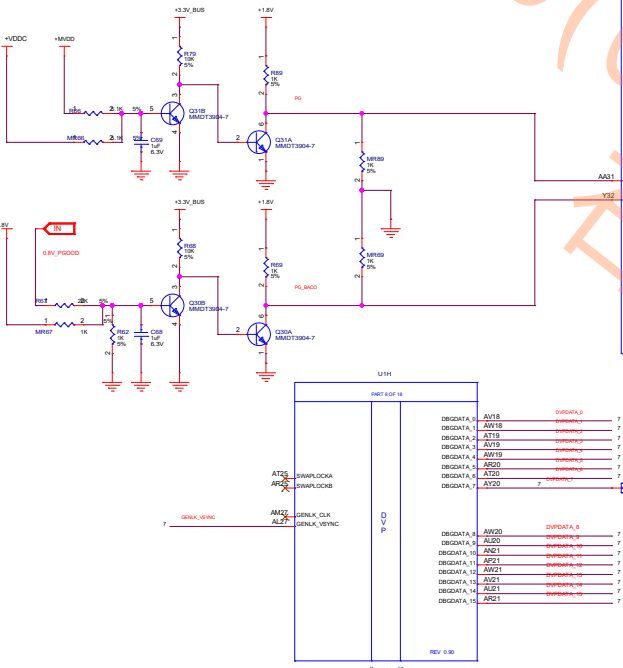


(6) GDDR5 MEMORY CH C/D

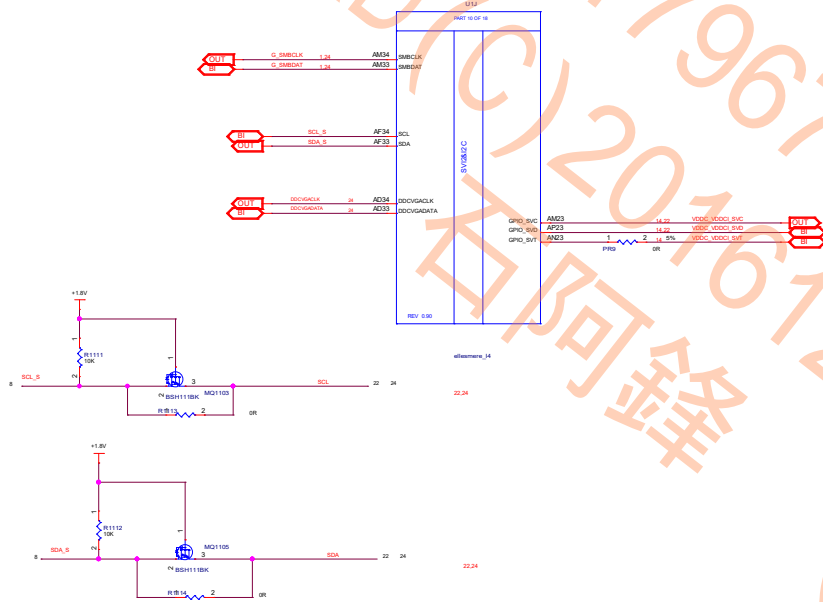


(7) ELLESMERE GPIO STRAP CF XTAL


I2C ADDRESS			FUNCTION	DEVICE
I2C ADDRESS			FUNCTION	DEVICE
0x48			EXT TEMP SENSOR	LM93C83



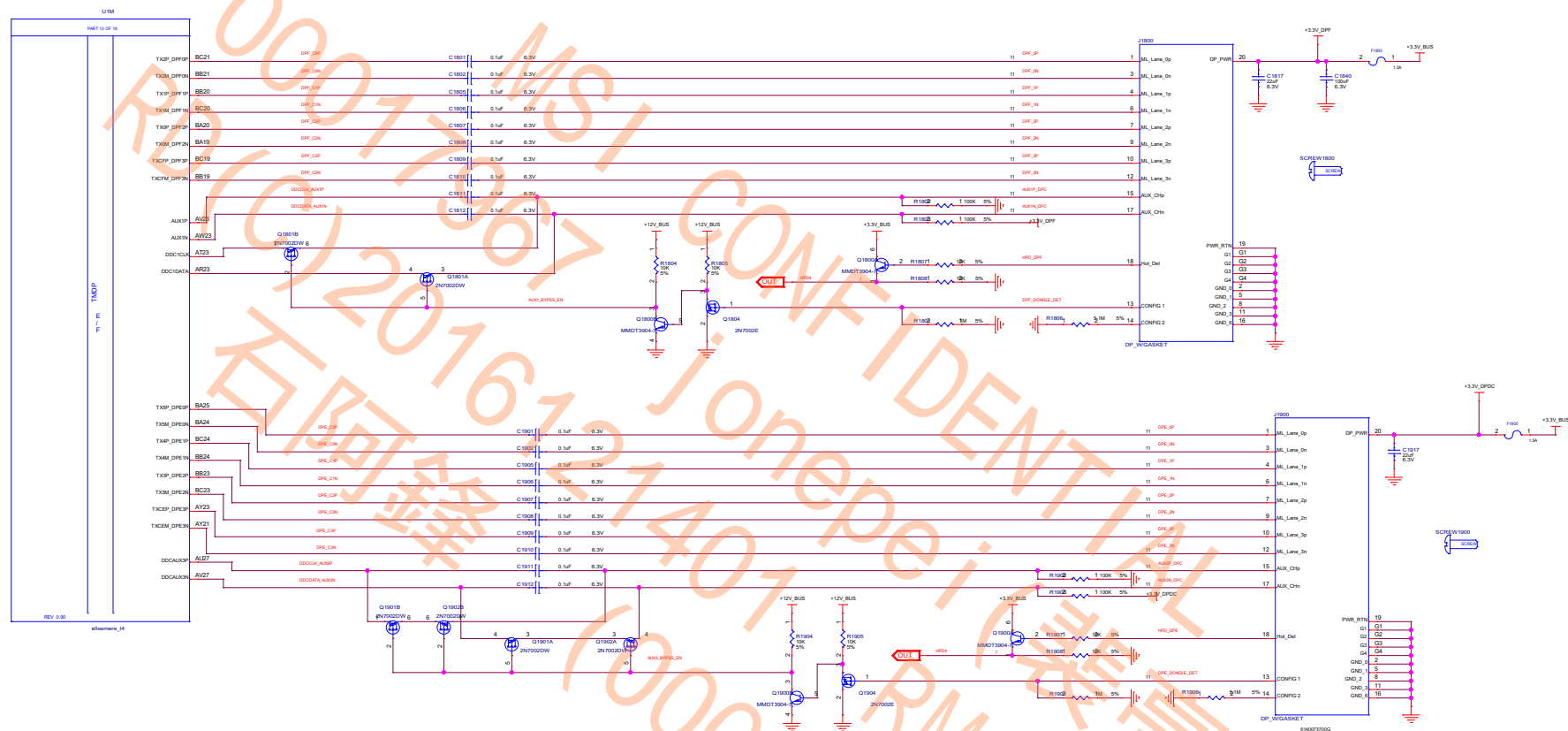
(8) ELLESMERE DAC1 LOCK



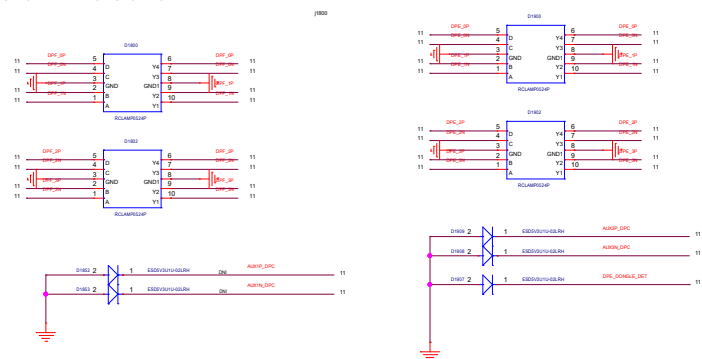


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	Size Custom	Document Description 0009 ELESMEERE TMDPAB dDVI		Rev 2.1
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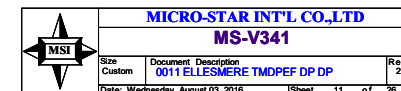




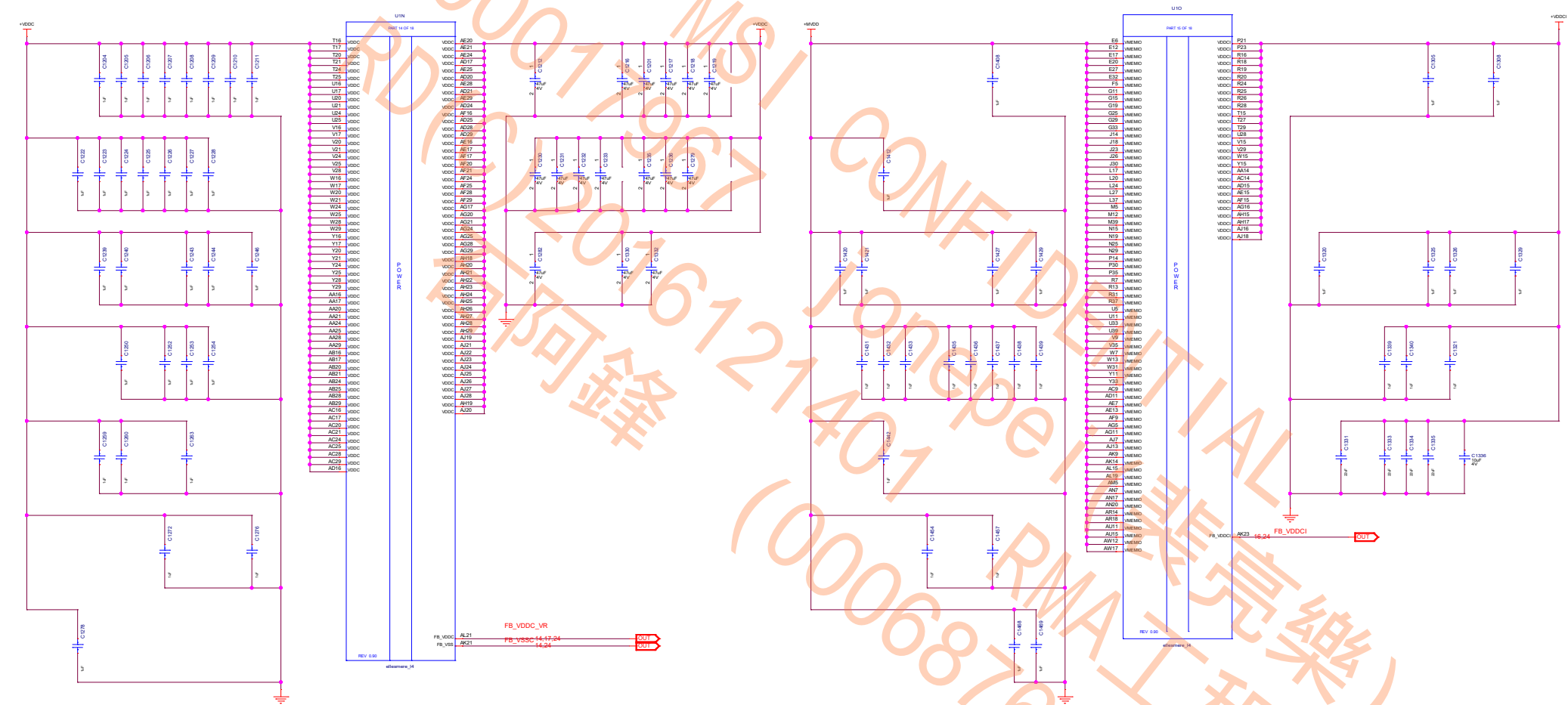
OPTIONAL ESD PROTECTION DIODES

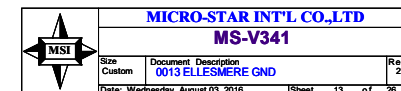


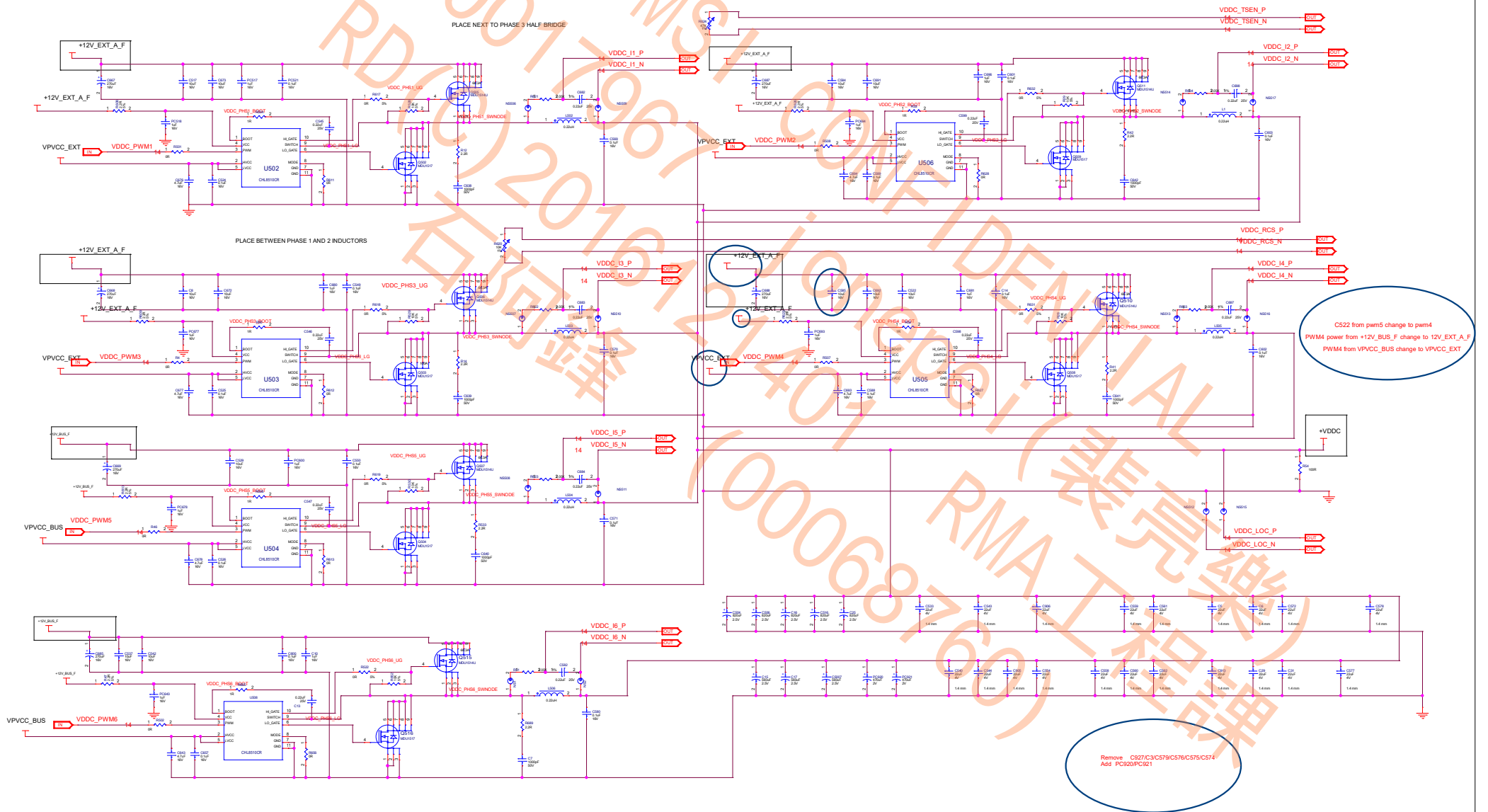
16ci203

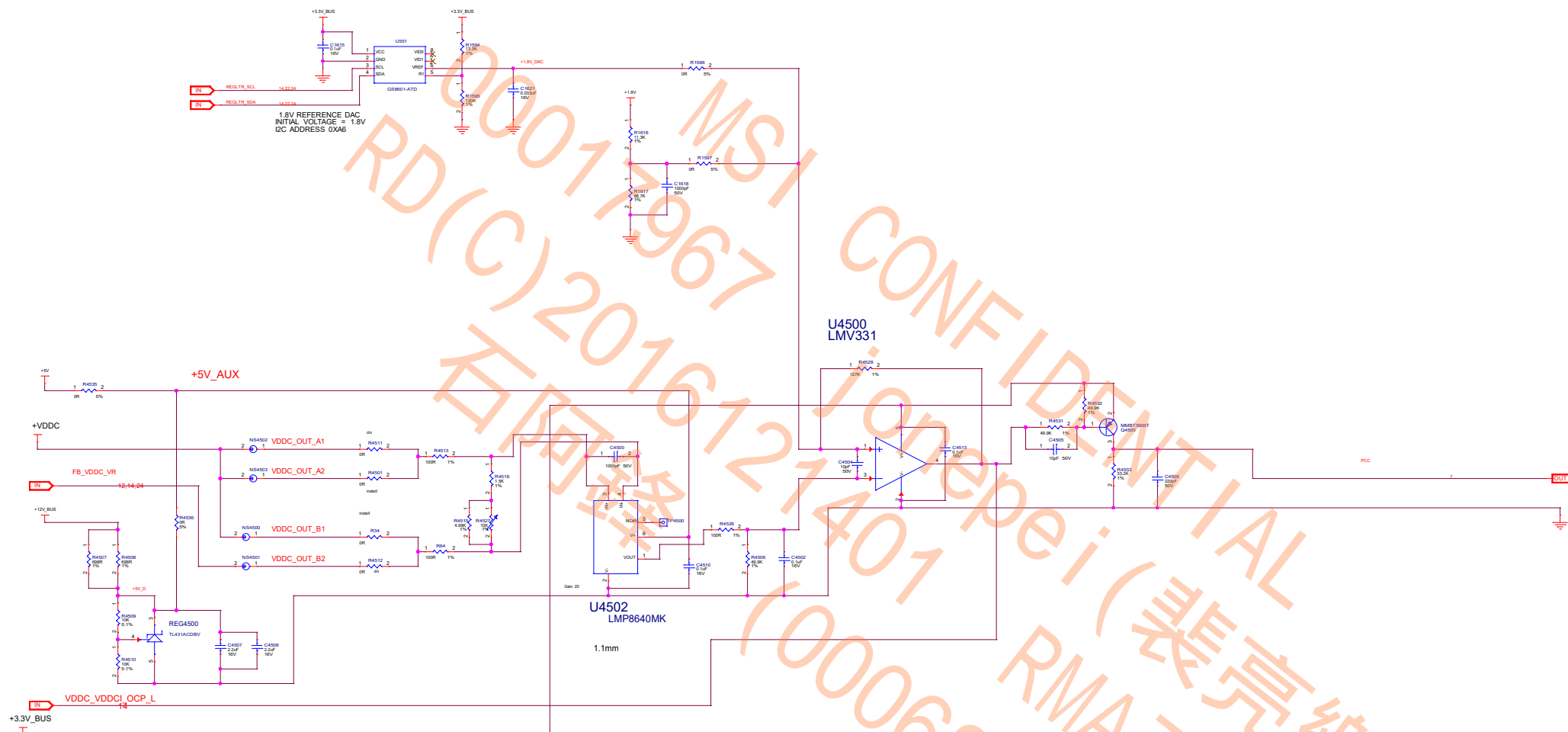


(12) ELLESMERE POWER

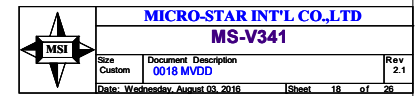


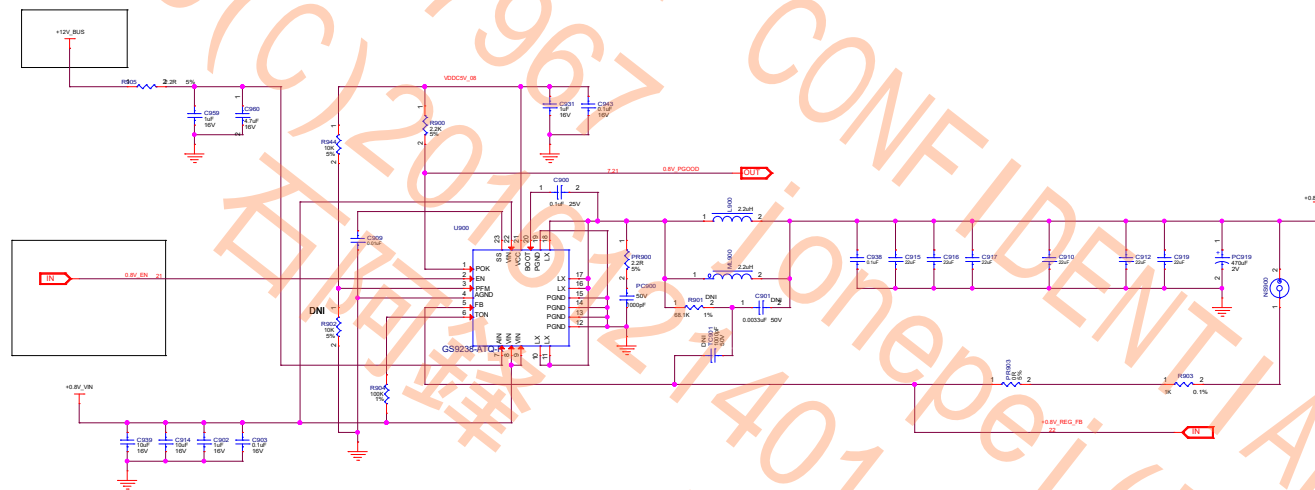




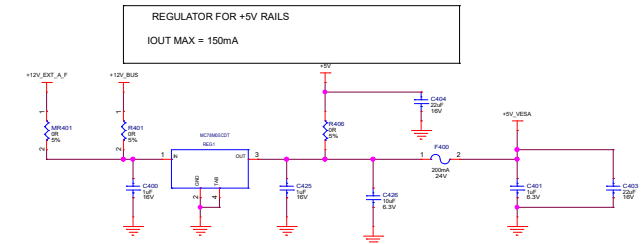
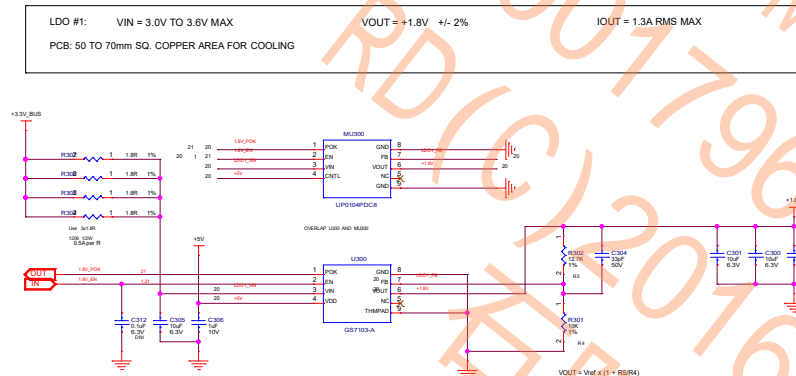


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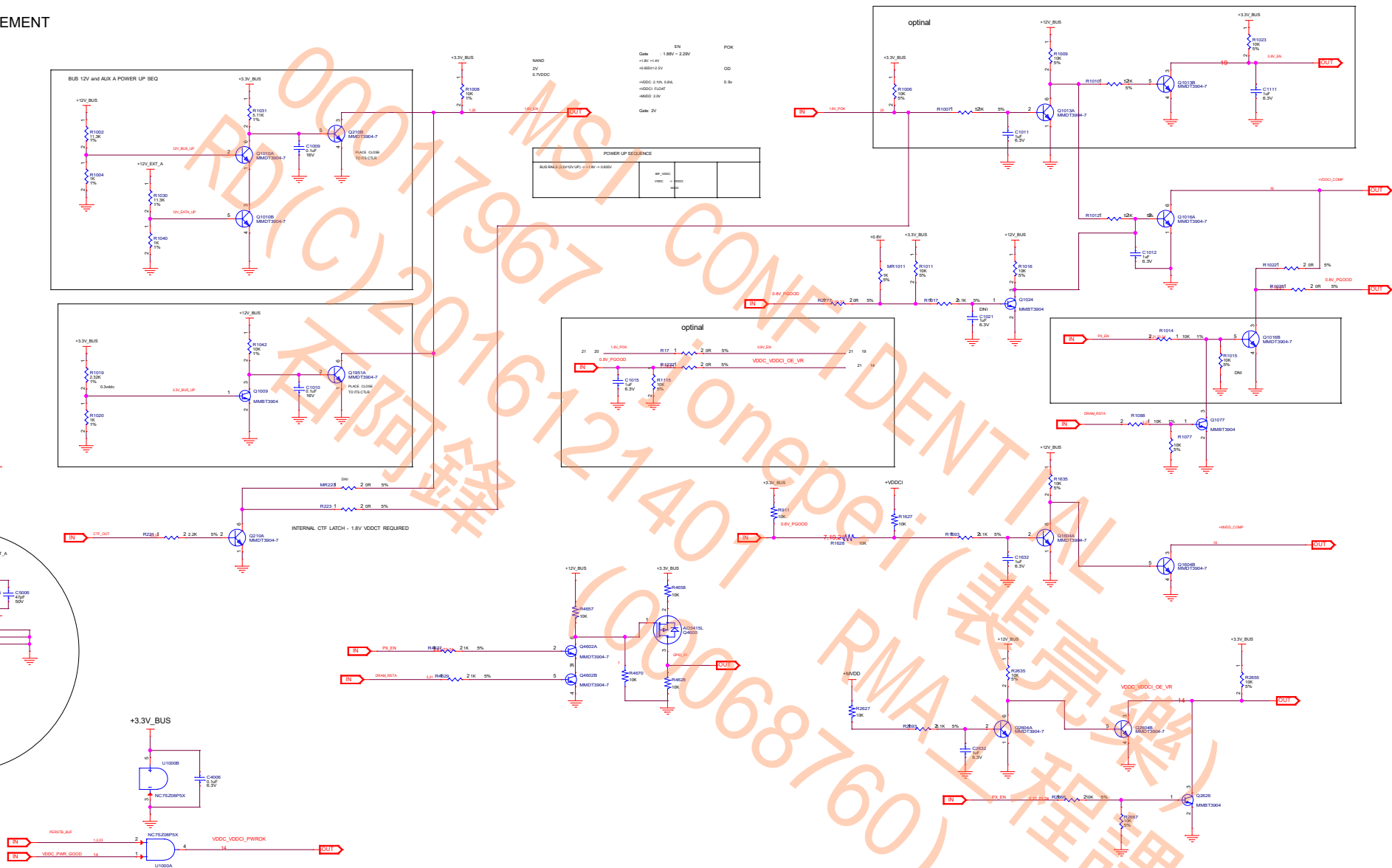


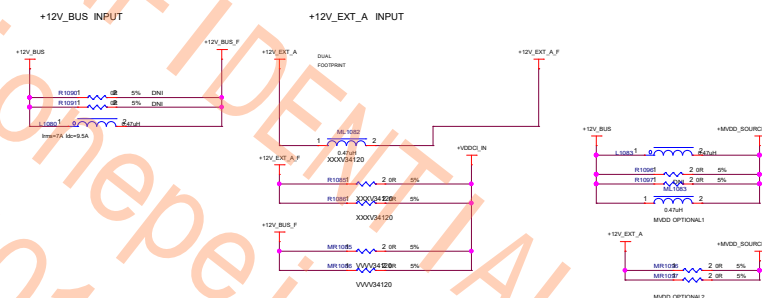
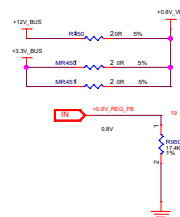
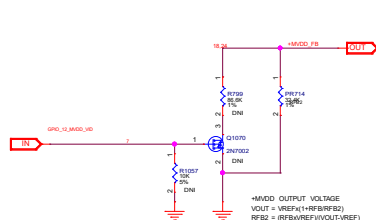
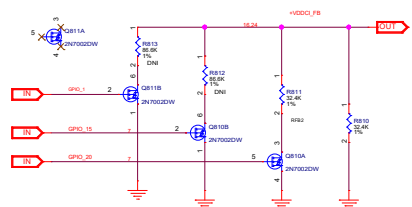
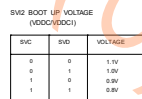


(18) SMALL RAIL REGULATORS

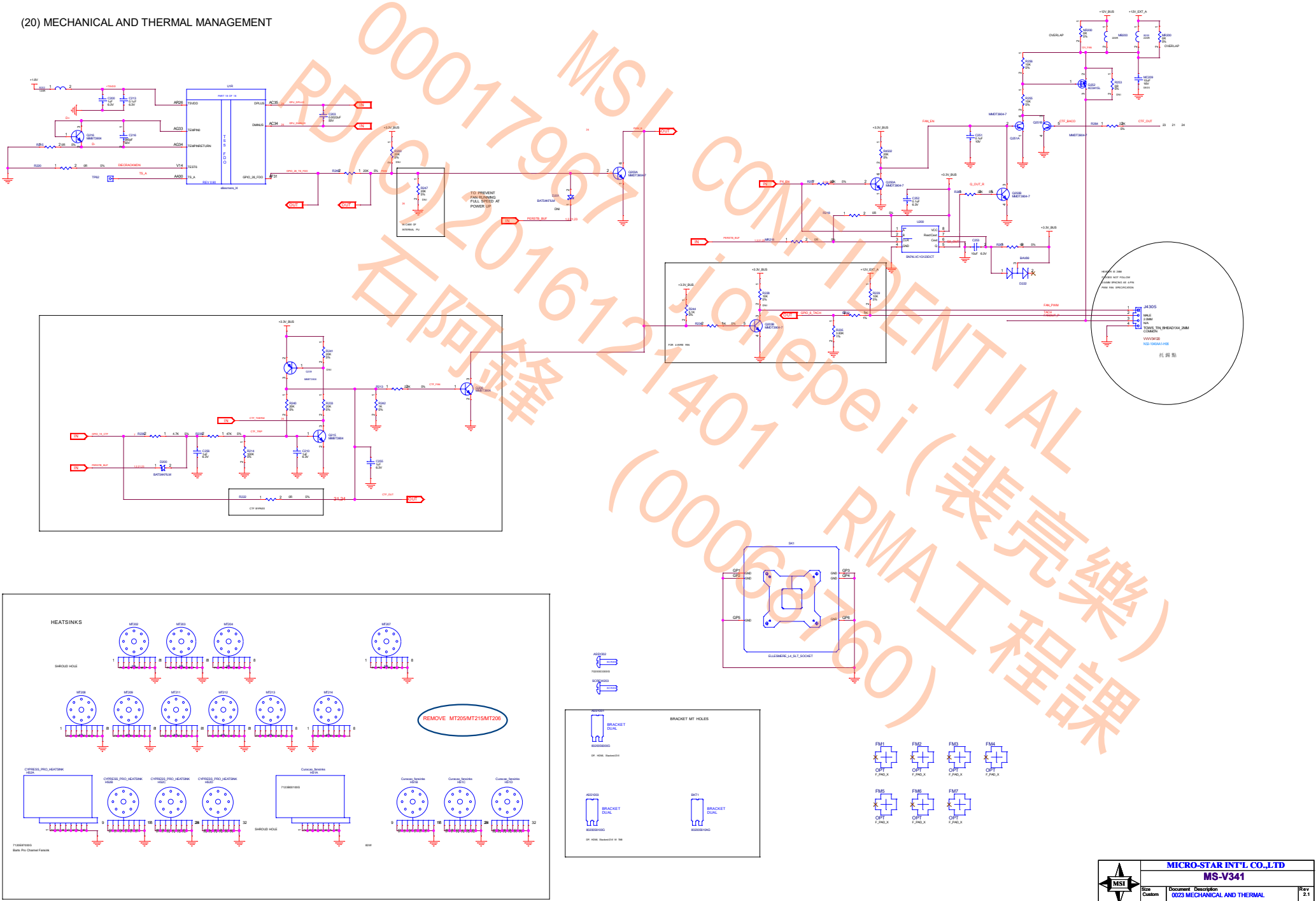


(19) POWER MANAGEMENT

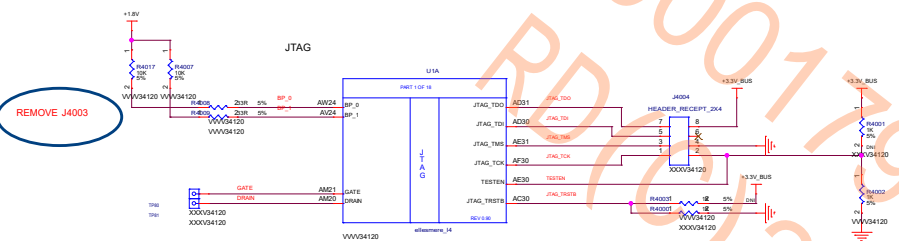




(20) MECHANICAL AND THERMAL MANAGEMENT



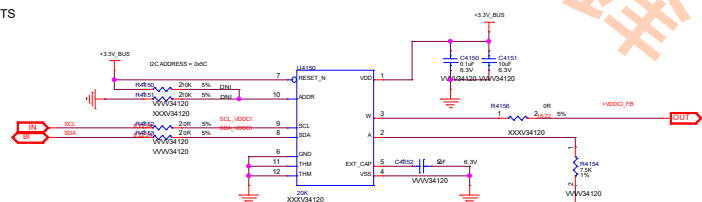
(21) DEBUG CIRCUITS



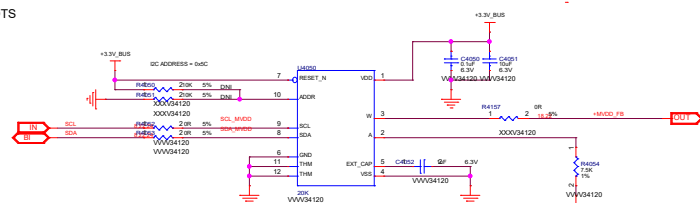
REMOVE

E-FUSE CAPABILITY
DEFAULT = GPIO-CONTROLLED
(MANUAL OPTION AS BACK-UP)

DIGITAL POTS



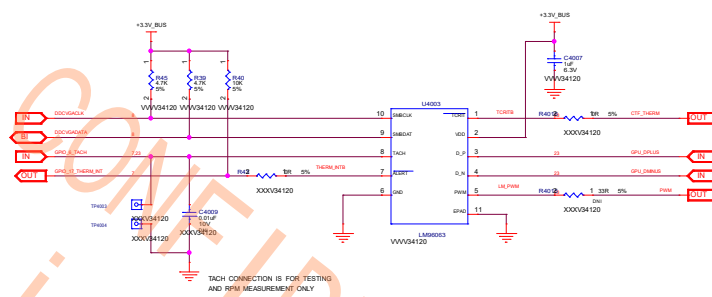
DIGITAL POTS



SWITCHES

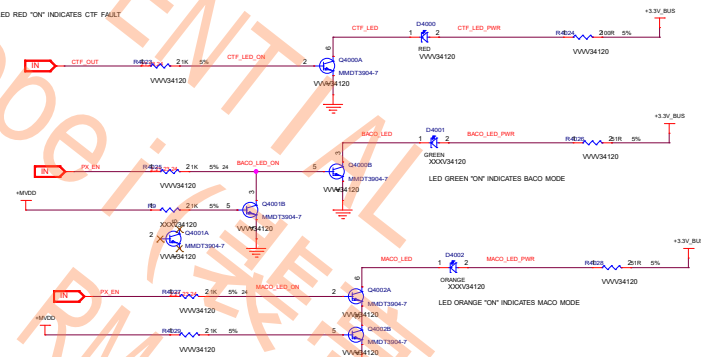


LM96163 FOR BACKUP THERMAL CONTROL

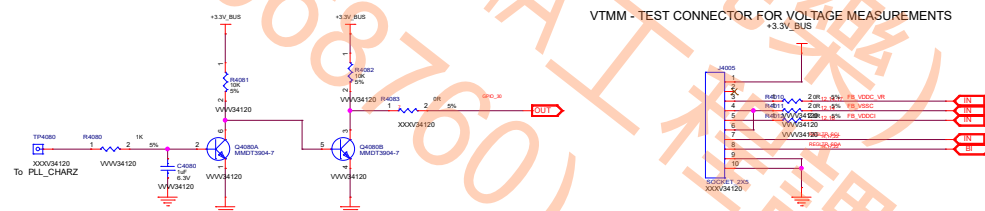



LED LIGHTS

LED RED "ON" INDICATES CTF FAULT



VTMM - TEST CONNECTOR FOR VOLTAGE MEASUREMENTS
+3.3V BUS



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23: EXT 6 PIN change to 反向 8 pin
REMOVE MT205/MT215/MT206

26: 1*4 托錫點

15: C522 from pwm5 change to pwm4
PWM4 power from +12V_BUS_F change to 12V_EXT_A_F
PWM4 from VPVCC_BUS change to VPVCC_EXT
Remove C927/C3/C579/C576/C575/C574
Add PC920/PC921

24: REMOVE J4003
REMOVE E-FUSE CAPABILITY